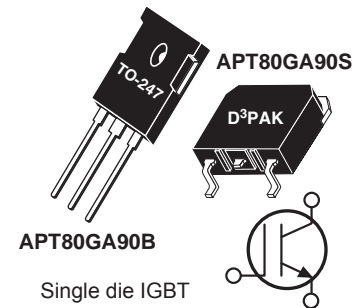



## High Speed PT IGBT

POWER MOS 8® is a high speed Punch-Through switch-mode IGBT. Low  $E_{off}$  is achieved through leading technology silicon design and lifetime control processes. A reduced  $E_{off}$  -  $V_{CE(ON)}$  tradeoff results in superior efficiency compared to other IGBT technologies. Low gate charge and a greatly reduced ratio of  $C_{res}/C_{ies}$  provide excellent noise immunity, short delay times and simple gate drive. The intrinsic chip gate resistance and capacitance of the poly-silicone gate structure help control di/dt during switching, resulting in low EMI, even when switching at high frequency.



### FEATURES

- Fast switching with low EMI
- Very Low  $E_{off}$  for maximum efficiency
- Ultra low  $C_{res}$  for improved noise immunity
- Low conduction loss
- Low gate charge
- Increased intrinsic gate resistance for low EMI
- RoHS compliant 

### TYPICAL APPLICATIONS

- ZVS phase shifted and other full bridge
- Half bridge
- High power PFC boost
- Welding
- UPS, solar, and other inverters
- High frequency, high efficiency industrial

### Absolute Maximum Ratings

Symbol	Parameter	Ratings	Unit
$V_{ces}$	Collector Emitter Voltage	900	V
$I_{C1}$	Continuous Collector Current @ $T_c = 25^\circ\text{C}$	145	A
$I_{C2}$	Continuous Collector Current @ $T_c = 100^\circ\text{C}$	80	
$I_{CM}$	Pulsed Collector Current <sup>1</sup>	239	
$V_{GE}$	Gate-Emitter Voltage <sup>2</sup>	±30	V
$P_D$	Total Power Dissipation @ $T_c = 25^\circ\text{C}$	625	W
SSOA	Switching Safe Operating Area @ $T_j = 150^\circ\text{C}$	239A @ 900V	
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to 150	°C
$T_L$	Lead Temperature for Soldering: 0.063" from Case for 10 Seconds	300	

### Static Characteristics

$T_J = 25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{BR(CEs)}$	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V, I_C = 1.0mA$	900			V
$V_{CE(on)}$	Collector-Emitter On Voltage	$V_{GE} = 15V, I_C = 47A$		2.5	3.1	
		$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$		2.2		
$V_{GE(th)}$	Gate Emitter Threshold Voltage	$V_{GE} = V_{CE}, I_C = 1mA$	3	4.5	6	
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{CE} = 900V, V_{GE} = 0V$			250	µA
		$T_J = 25^\circ\text{C}$ $T_J = 125^\circ\text{C}$			1000	
$I_{GES}$	Gate-Emitter Leakage Current	$V_{GS} = \pm 30V$			±100	nA

### Thermal and Mechanical Characteristics

Symbol	Characteristic	Min	Typ	Max	Unit
$R_{\theta JC}$	Junction to Case Thermal Resistance	-	-	0.2	°C/W
$W_T$	Package Weight	-	5.9	-	g
Torque	Mounting Torque (TO-247 Package), 4-40 or M3 screw			10	in-lbf

## Dynamic Characteristics

 $T_J = 25^\circ\text{C}$  unless otherwise specified

APT80GA90B\_S

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$C_{ies}$	Input Capacitance	Capacitance $V_{GE} = 0V, V_{CE} = 25V$ $f = 1\text{MHz}$		4560		pF
$C_{oes}$	Output Capacitance			411		
$C_{res}$	Reverse Transfer Capacitance			62		
$Q_g^3$	Total Gate Charge	Gate Charge $V_{GE} = 15V$ $V_{CE} = 450V$ $I_C = 47A$		200		nC
$Q_{ge}$	Gate-Emitter Charge			30		
$Q_{gc}$	Gate-Collector Charge			72		
SSOA	Switching Safe Operating Area	$T_J = 150^\circ\text{C}, R_G = 4.7\Omega^4, V_{GE} = 15V,$ $L = 100\mu\text{H}, V_{CE} = 900V$	239			A
$t_{d(on)}$	Turn-On Delay Time	Inductive Switching ( $25^\circ\text{C}$ ) $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 47A$ $R_G = 4.7\Omega^4$ $T_J = +25^\circ\text{C}$		18		ns
$t_r$	Current Rise Time			29		
$t_{d(off)}$	Turn-Off Delay Time			149		
$t_f$	Current Fall Time			85		
$E_{on2}$	Turn-On Switching Energy			1652		
$E_{off}^6$	Turn-Off Switching Energy		1389		$\mu\text{J}$	
$t_{d(on)}$	Turn-On Delay Time	Inductive Switching ( $125^\circ\text{C}$ ) $V_{CC} = 600V$ $V_{GE} = 15V$ $I_C = 47A$ $R_G = 4.7\Omega^4$ $T_J = +125^\circ\text{C}$		18		ns
$t_r$	Current Rise Time			31		
$t_{d(off)}$	Turn-Off Delay Time			192		
$t_f$	Current Fall Time			128		
$E_{on2}$	Turn-On Switching Energy			2813		
$E_{off}^6$	Turn-Off Switching Energy			2082		

1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.

2 Pulse test: Pulse Width <  $380\mu\text{s}$ , duty cycle < 2%.

3 See Mil-Std-750 Method 3471.

4  $R_G$  is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)

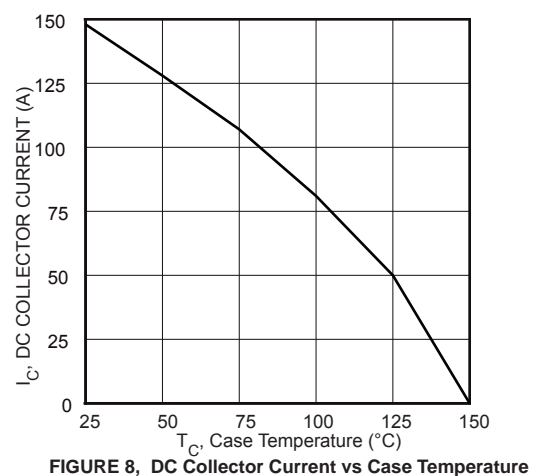
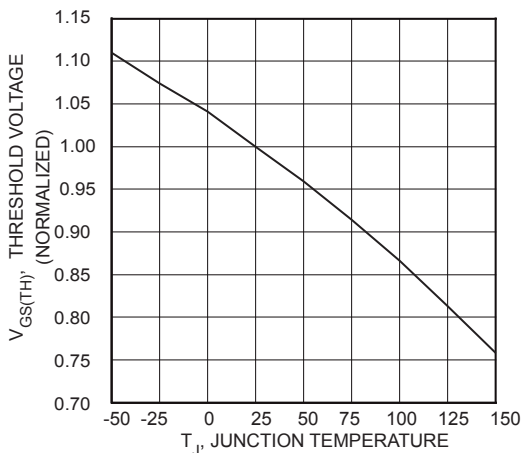
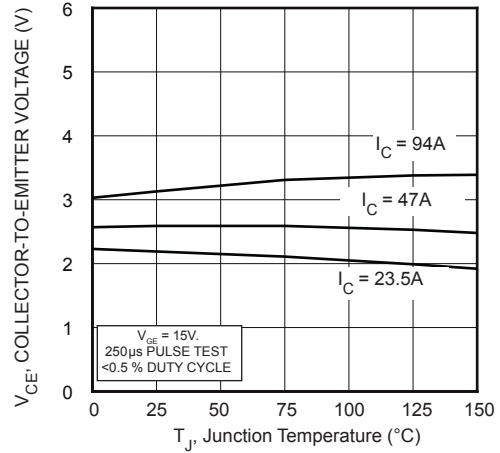
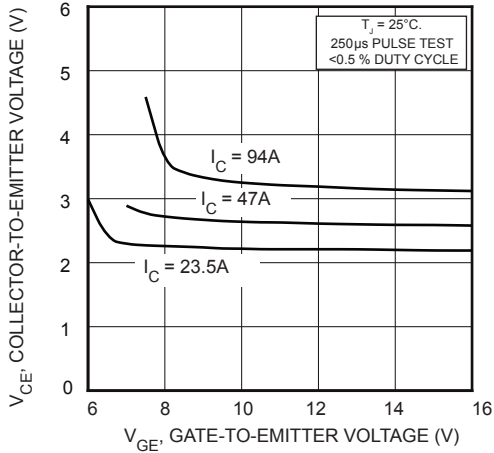
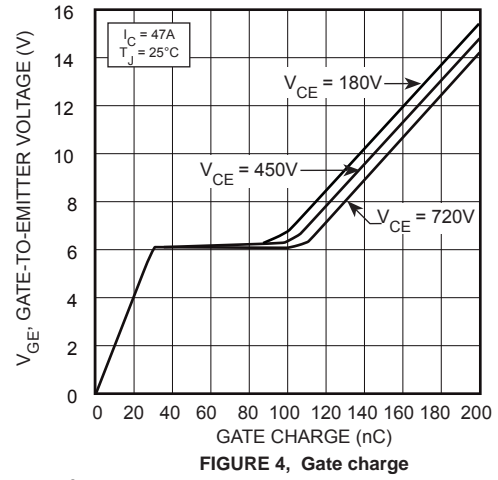
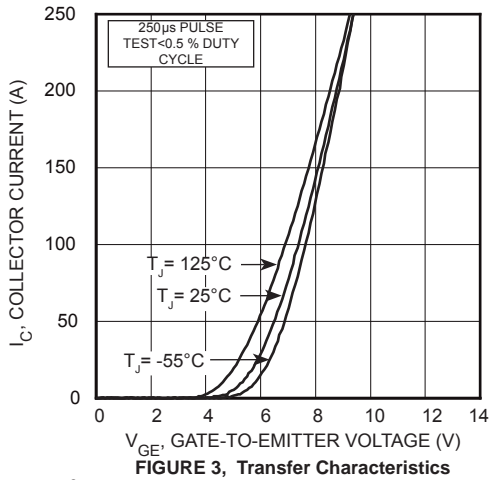
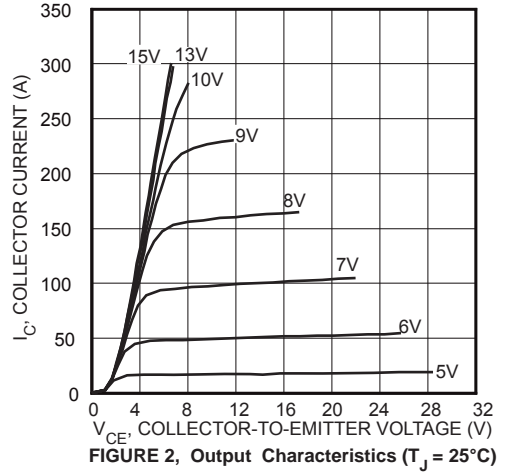
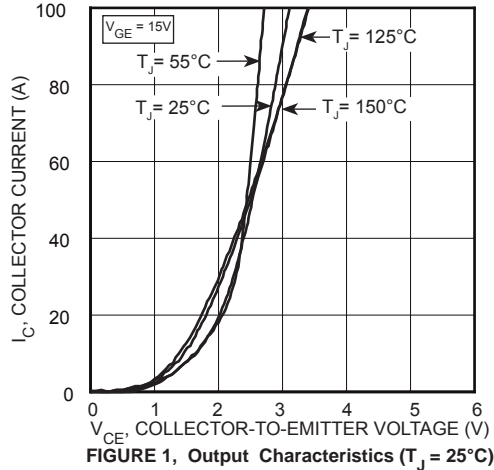
5  $E_{on2}$  is the clamped inductive turn on energy that includes a commutating diode reverse recovery current in the IGBT turn on energy loss. A combi device is used for the clamping diode.

6  $E_{off}$  is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

**Microsemi reserves the right to change, without notice, the specifications and information contained herein.**

Typical Performance Curves

APT80GA90B\_S



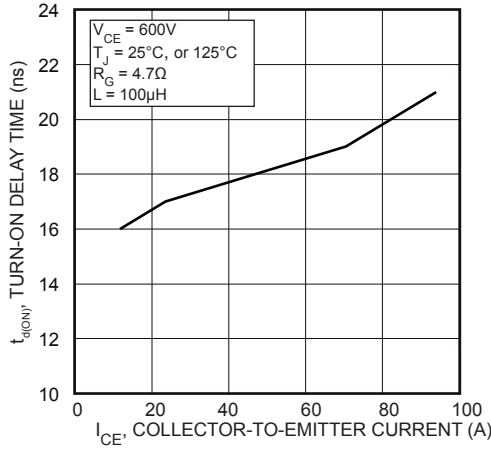


FIGURE 9, Turn-On Delay Time vs Collector Current

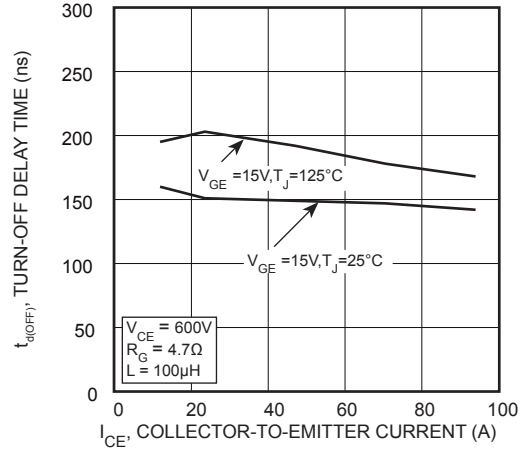


FIGURE 10, Turn-Off Delay Time vs Collector Current

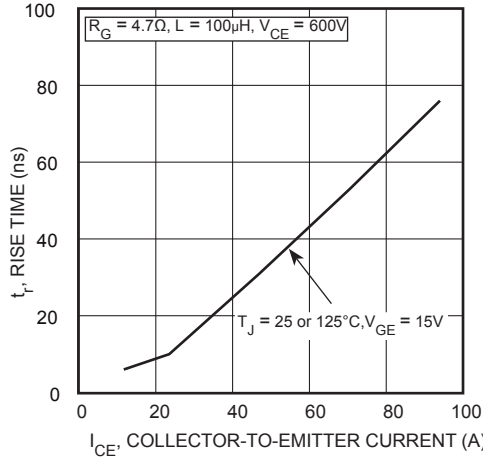


FIGURE 11, Current Rise Time vs Collector Current

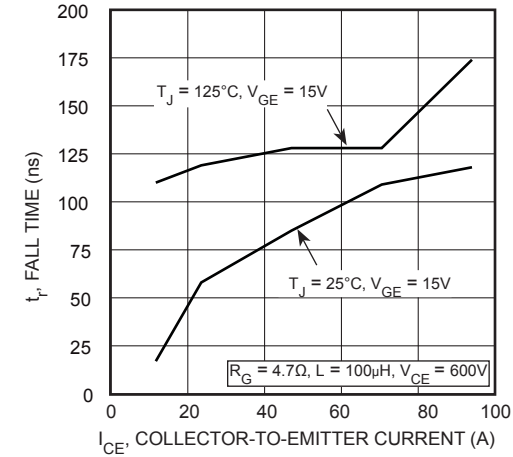


FIGURE 12, Current Fall Time vs Collector Current

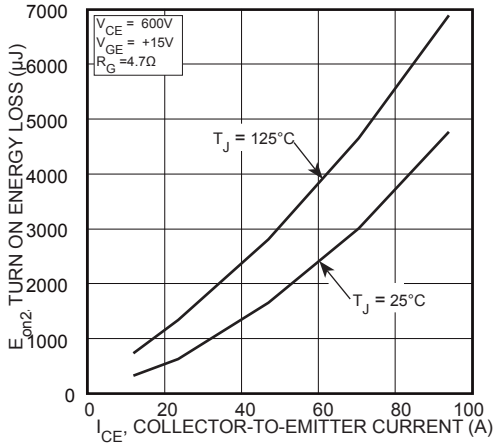


FIGURE 13, Turn-On Energy Loss vs Collector Current

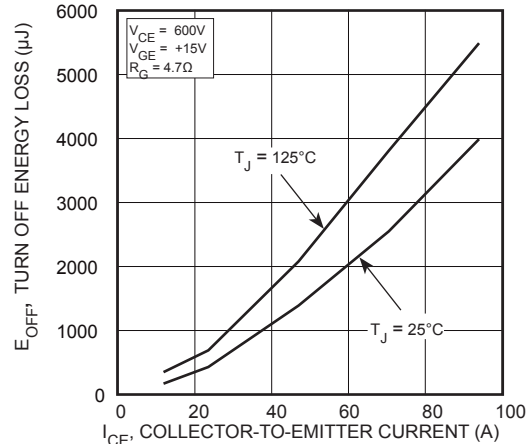


FIGURE 14, Turn-Off Energy Loss vs Collector Current

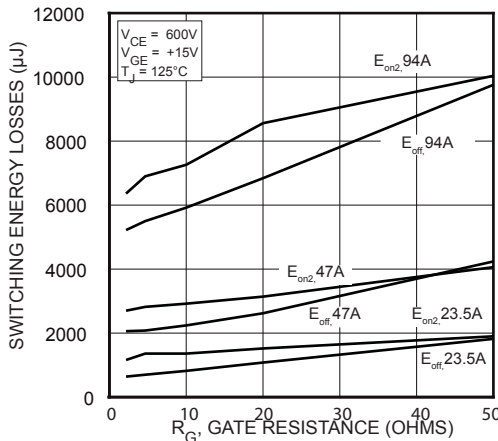


FIGURE 15, Switching Energy Losses vs Gate Resistance

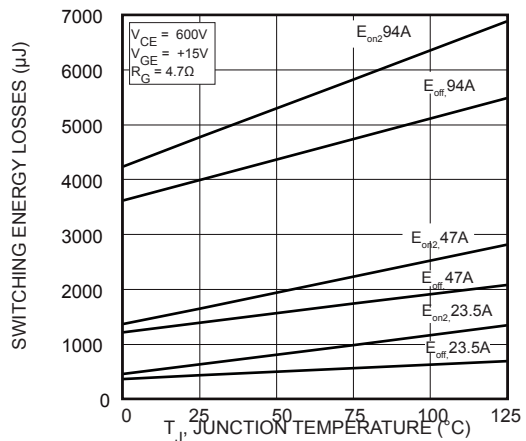


FIGURE 16, Switching Energy Losses vs Junction Temperature

# Typical Performance Curves

APT80GA90B\_S

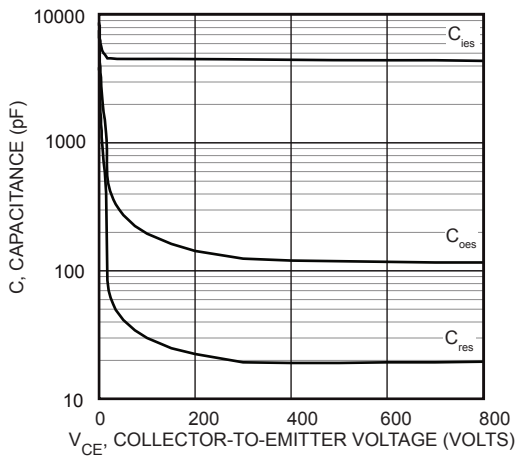


FIGURE 17, Capacitance vs Collector-To-Emitter Voltage

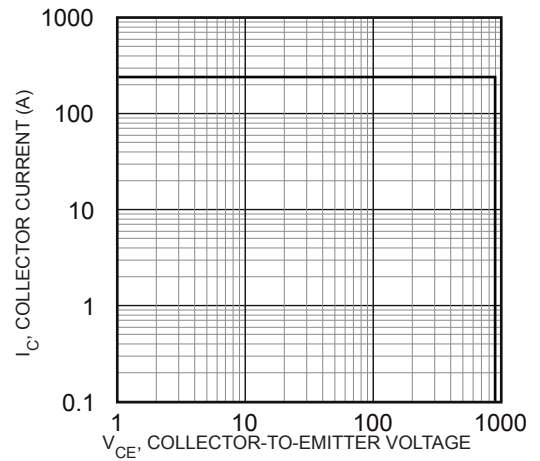


FIGURE 18, Minimum Switching Safe Operating Area

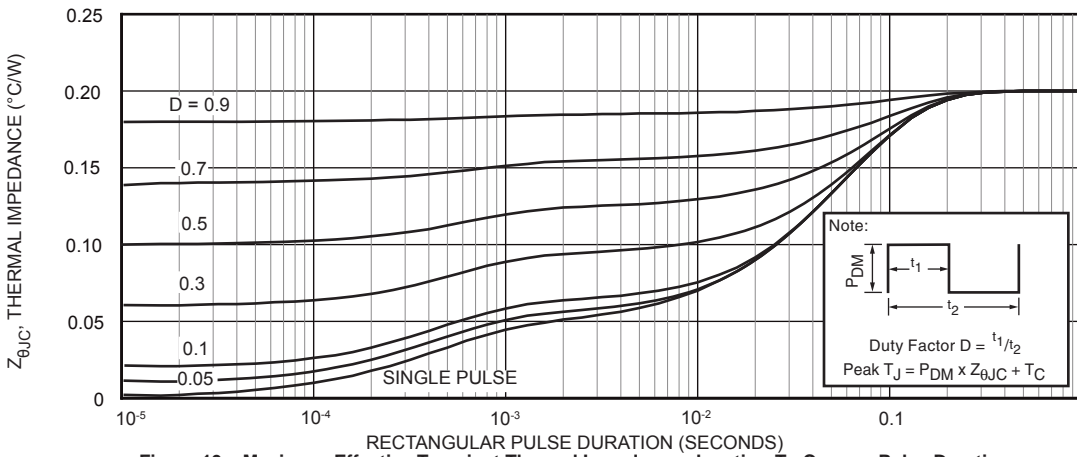


Figure 19a, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

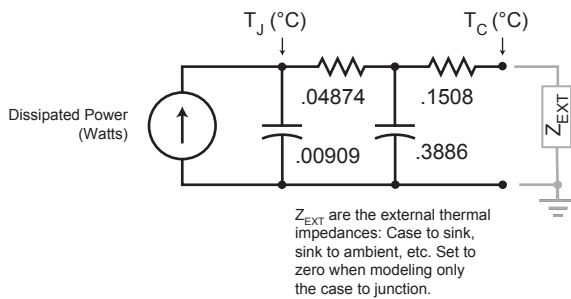


FIGURE 19b, TRANSIENT THERMAL IMPEDANCE MODEL

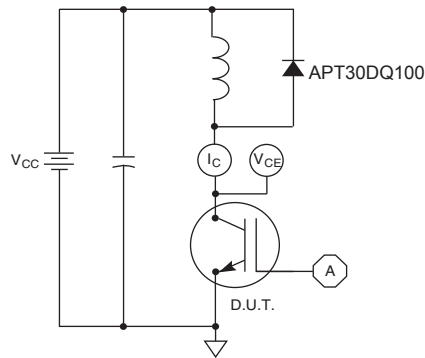


Figure 20, Inductive Switching Test Circuit

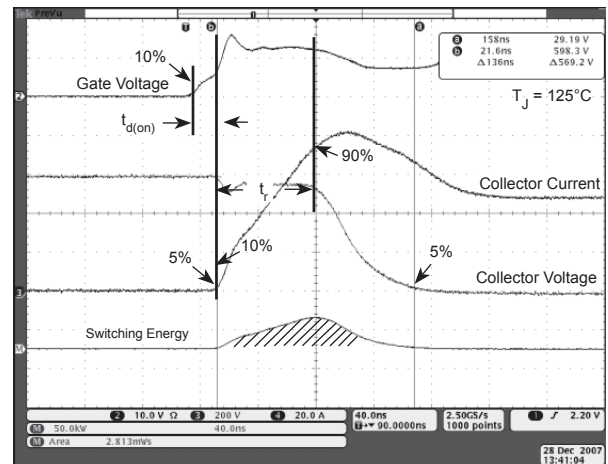


Figure 21, Turn-on Switching Waveforms and Definitions

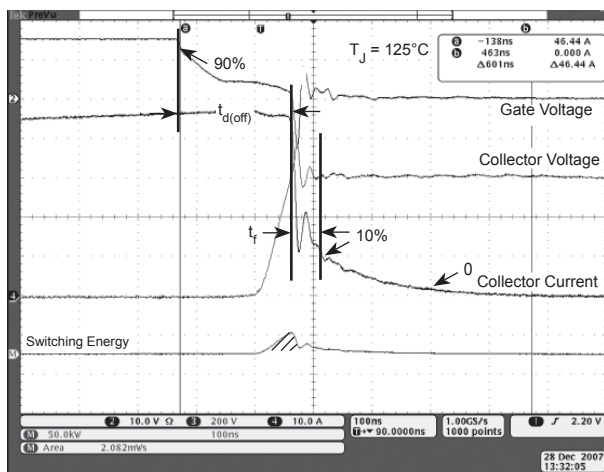
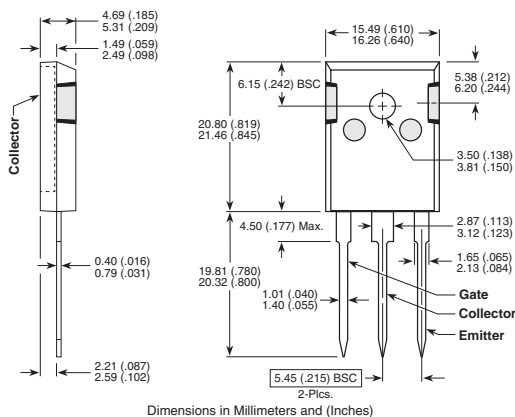


Figure 22, Turn-off Switching Waveforms and Definitions

TO-247 (B) Package Outline



D<sup>3</sup>PAK Package Outline

e3 100% Sn Plated

